



SECTION 2 FORM PTO 1449 - MODIFIED

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Heinrich et al.

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Serial No: 09/329,557

Date: February 11, 2000

Date Filed: June 10, 1999

Invention: SYSTEM FOR REDUCING ALIASING ON A DISPLAY DEVICE

Assistant Commissioner for Patents
Washington, DC 20231

LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

U.S. Documents

Exam. Ref.	Document	Issue			
<u>Init.</u>	<u>No.</u>	<u>Number</u>	<u>Date</u>	<u>Name</u>	<u>Class</u>
PN PN	AX	4,434,437	Feb. 28, 1984	Strolle et al.	358/140
	AY	4,615,013	Sep. 30, 1986	Yan et al.	364/521
	AZ	4,646,232	Feb. 24, 1987	Chang et al.	364/200
	BA	4,908,780	March 13, 1990	Priem et al.	364/521
	BB	4,918,626	April 17, 1990	Watkins et al.	364/522
	BC	4,991,122	Feb. 5, 1991	Sanders	364/521
	BD	5,107,415	April 21, 1992	Sato et al.	395/800
	BE	5,123,085	June 16, 1992	Wells et al.	395/121

Examiner: P. Nguyen

Date Considered: 8/20/02

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PN	BF	5,239,654	Aug. 24, 1993	Ing-Simmons et al.	395/800
	BG	5,287,438	Feb. 15, 1994	Kelleher	395/132
	BH	5,293,480	March 8, 1994	Miller et al.	395/163
	BI	5,313,551	May 17, 1994	Labrousse et al.	395/425
	BJ	5,363,475	Nov. 8, 1994	Baker et al.	395/122
	BK	5,371,840	Dec. 6, 1994	Fischer et al.	395/133
	BL	5,394,524	Feb. 28, 1995	DiNicola et al.	395/163
	BM	5,398,328	March 14, 1995	Weber et al.	395/500
	BN	5,446,479	Aug. 29, 1995	Thompson et al.	345/139
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	CA	5,701,365	Dec. 23, 1997	Harrington et al.	382/212
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	CE	5,757,375	May 26, 1998	Kawase	345/429
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Foreign Patent Documents

<u>Exam. Ref.</u> <u>Init. No.</u>	<u>Document</u> <u>Number</u>	<u>Issue</u> <u>Date</u>	<u>Country</u>
<i>PN</i> CM	WO 86/07646	Dec. 31, 1986	World
CN	0 311 798 A2	April 19, 1989	Europe
CO	0 397 180 A2	Nov. 14, 1990	Europe
CP	0 438 194 A2	July 24, 1991	Europe
CQ	0 448 286 A2	Sept. 25, 1991	Europe
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Other Documents

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PN DG "A Dataflow Image Processing System TIP-4," Fujita et al., C&C Information Technology Research Labs, NEC Corporation, September 1989, pp. 735-741.

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PN DI "Effective Cache Mechanism for Texture Mapping," IBM Technical Disclosure Bulletin, Vol. 39, No. 12, December 1996, pp. 213-217.

PN DJ "Advanced Raster Graphics Architecture," ~~XP-002118066~~, pp.890-893, *1990*

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PN DL "Address Munging Support in a Memory Controller/PCI Host Bridge for the PowerPC 603 CPU Operating in 32-Bit Data Mode," IBM Technical Disclosure Bulletin, Vol. 38, No. 09, September 1995, pp. 237-240.

PN DM "One Frame Ahead: Frame Buffer Management for Animation and Real-Time Graphics," ~~XP-000749898~~, Auel et al., Tektronix Inc., pp. 43-50, *1988*

PN DN "Efficient Alias-Free Rendering Using Bit-Masks and Look-Up Tables," Abram et al., The University of North Carolina at Chapel Hill, XP-002115680, July 1985, pp. 53-59

PN DO "A New Simple and Efficient Antialiasing with Subpixel Masks," Schilling et al., Computer Graphics, Vol. 25, No. 4, July 1991, pp. 133-141.

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- PN DQ "The Reyes Image Rendering Architecture," Cook et al., Computer Graphics, Vol. 21, No. 4, July 1987, pp. 95-102.
- PN DR "The Accumulation Buffer: Hardware Support for High-Quality Rendering," Haeberli et al., Computer Graphics, Vol. 24, No. 4, August 1990, pp. 309-318.
- PN DS "Advanced Animation and Rendering Techniques," Watt et al., ACM Press, New York, New York, pp.127-137, 1988
- PN DT "The A-Buffer, an Antialiased Hidden Surface Method, Carpenter, Loren, Computer Graphics, Vol. 18, No. 3, July 1984, pp. 13-18.

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